Pf-2926/nec/us/mh

ABSTRACT OF THE DISCLOSURE

The present invention provides a method and a circuit for driving

a color liquid crystal display in a normal driving mode and a power saving

mode, wherein in the normal driving mode, voltages corresponding to

image display data are applied to data electrodes of the color liquid crystal

display, and wherein in the power saving mode, voltages corresponding to

highly significant bit signals of the image display data are applied as

display data signals to the data electrodes.